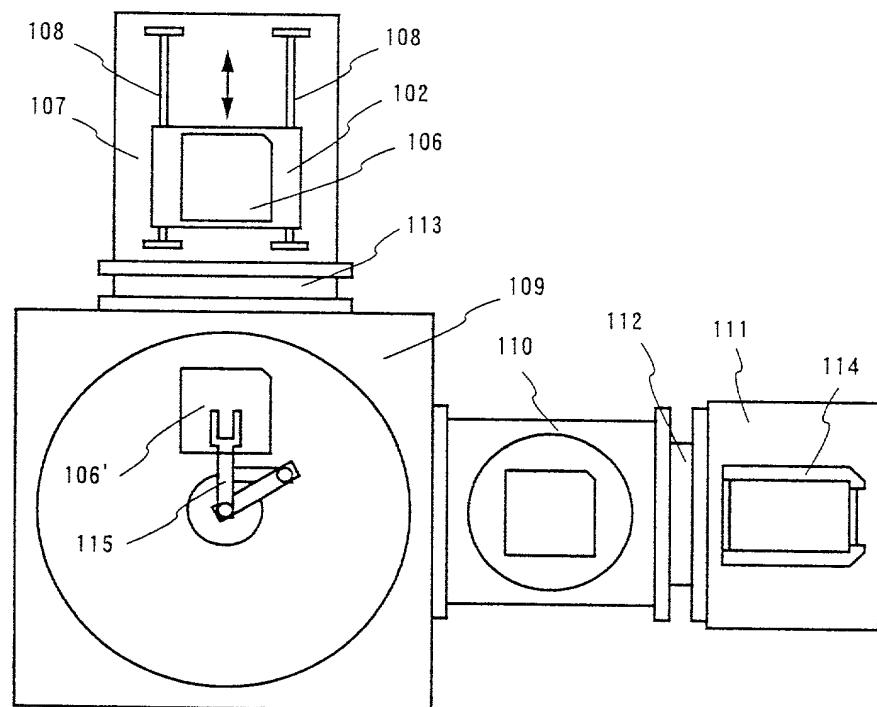
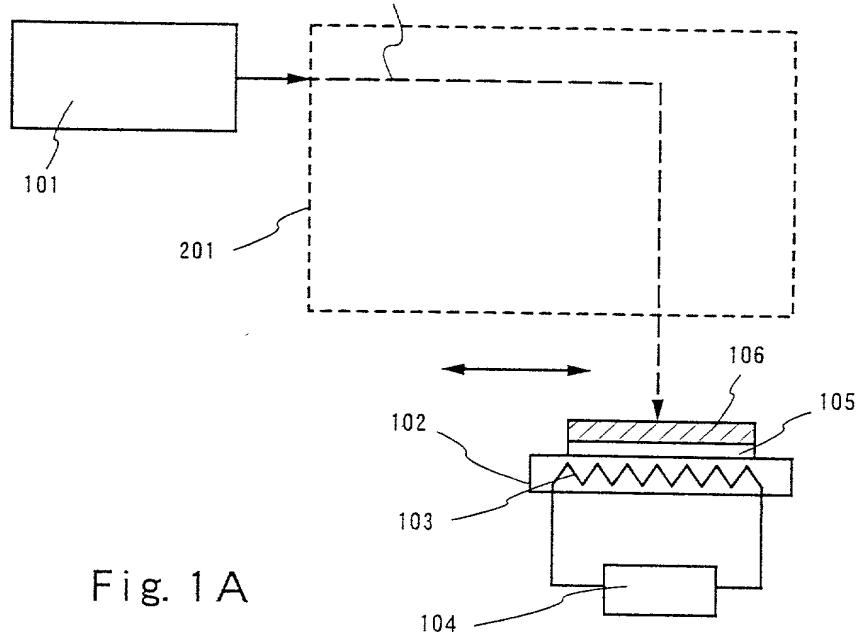


second, third, or fourth harmonic



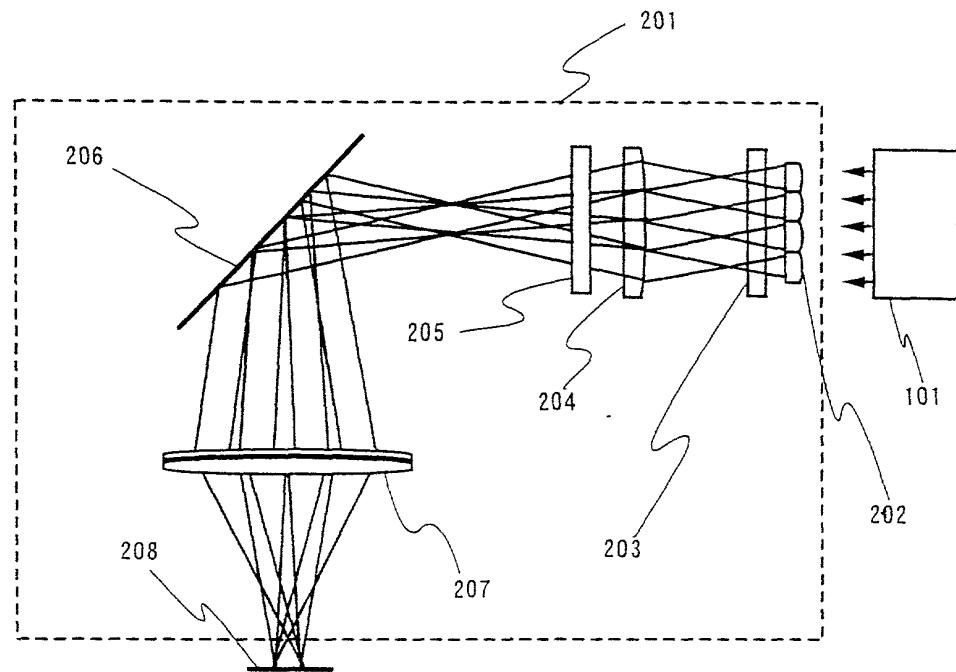


Fig. 2A

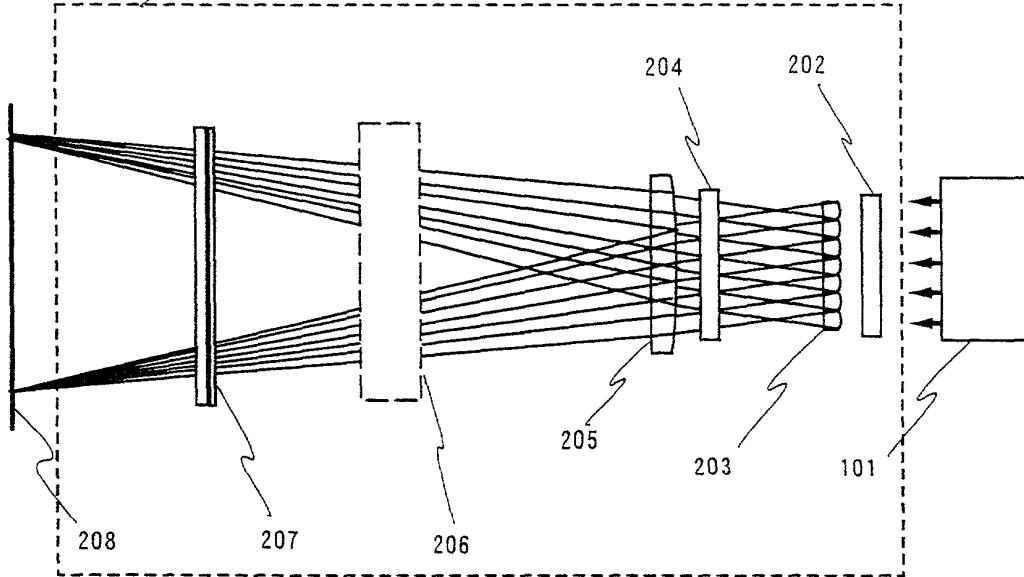


Fig. 2B

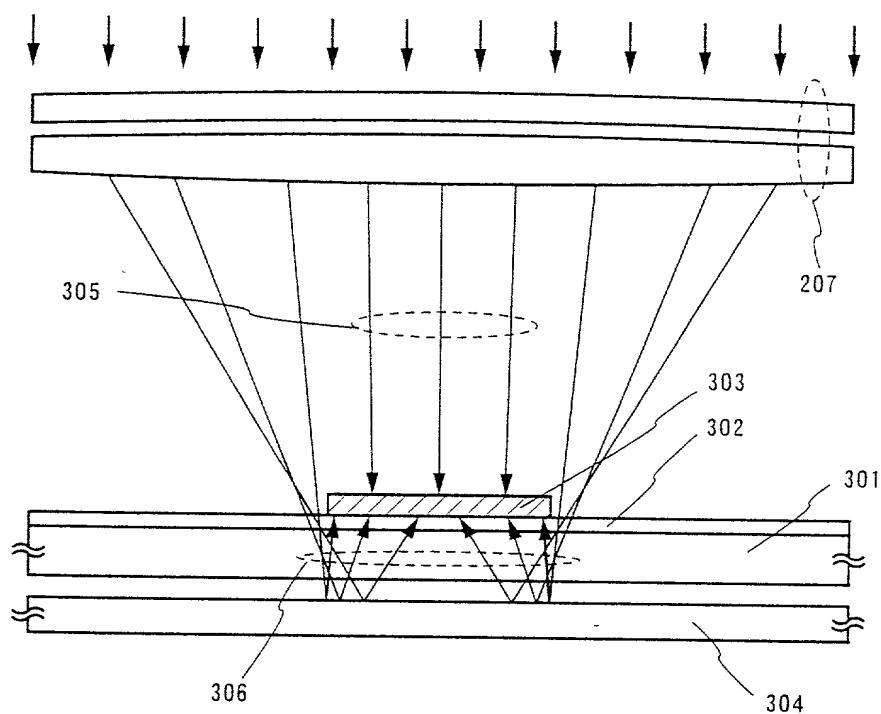


Fig. 3

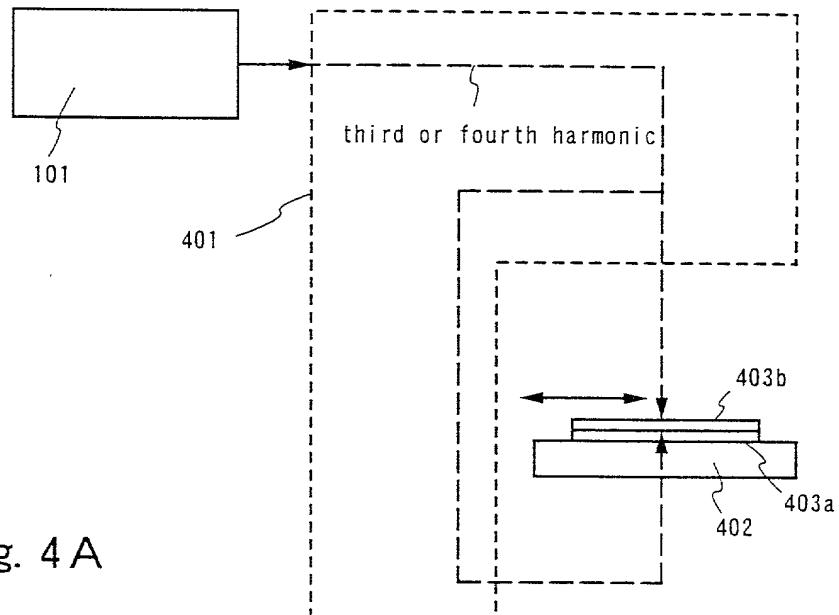


Fig. 4A

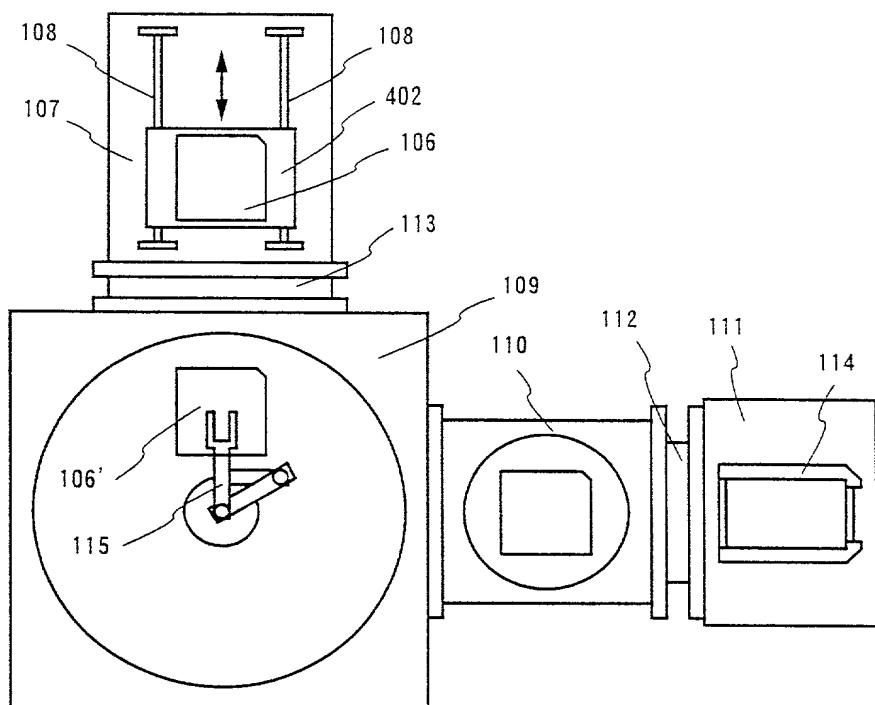


Fig. 4B

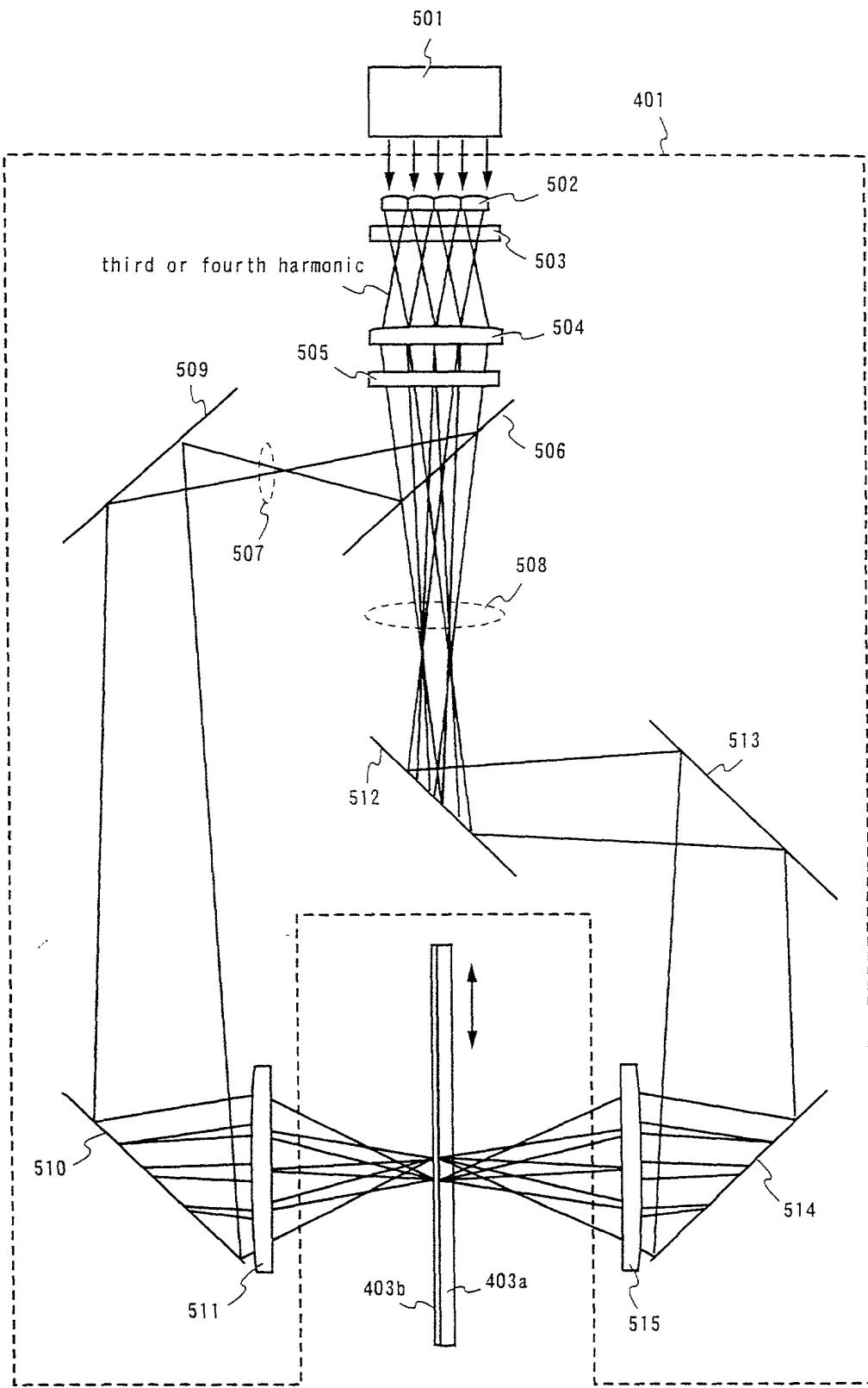


Fig. 5

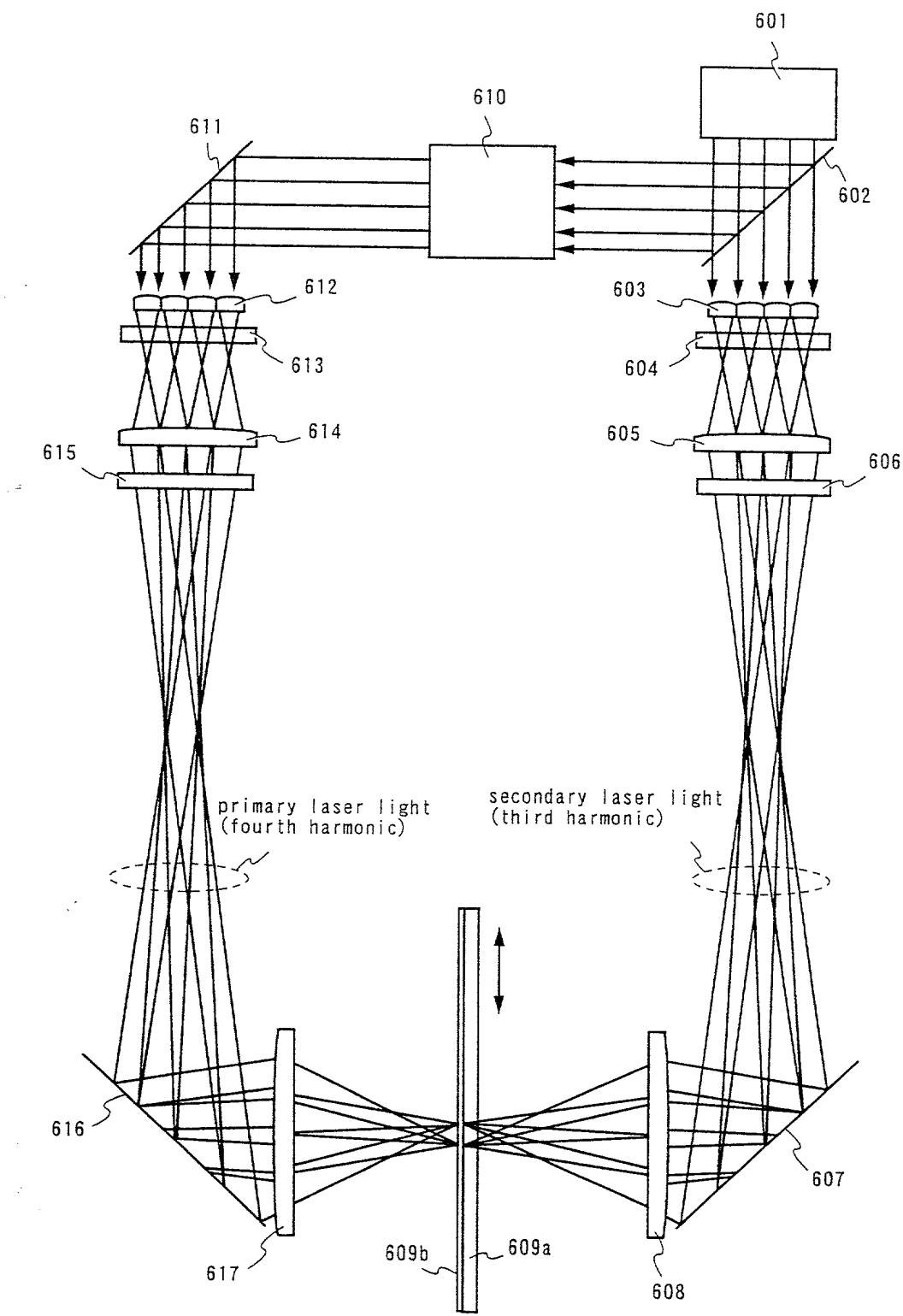


Fig. 6

Fig. 7A

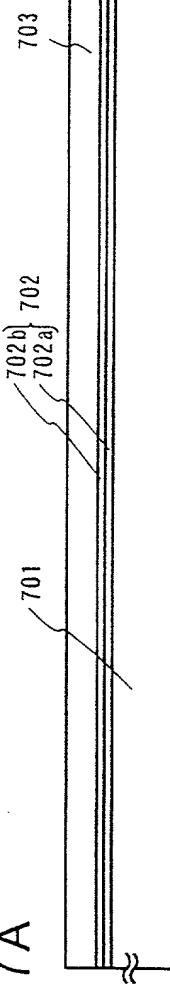


Fig. 7B

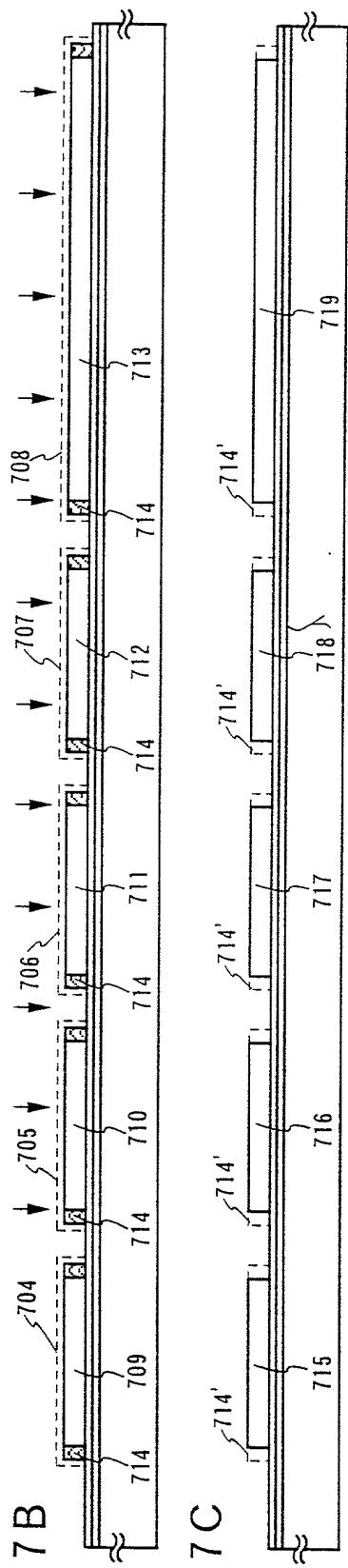


Fig. 7C

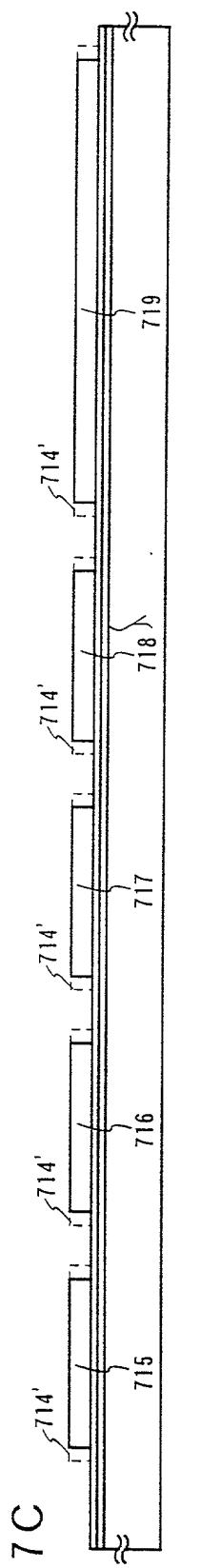


Fig. 7D

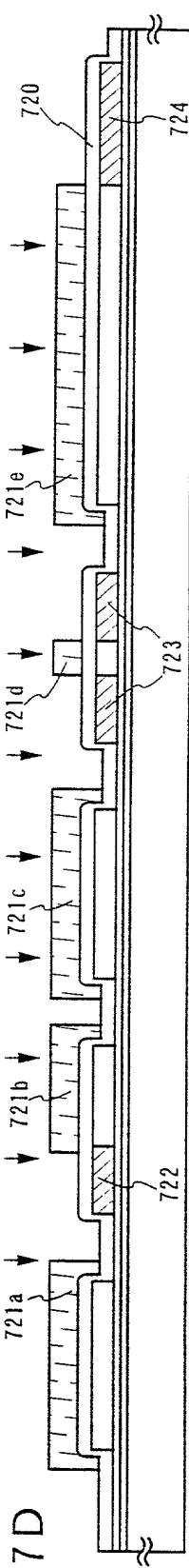


Fig. 7E

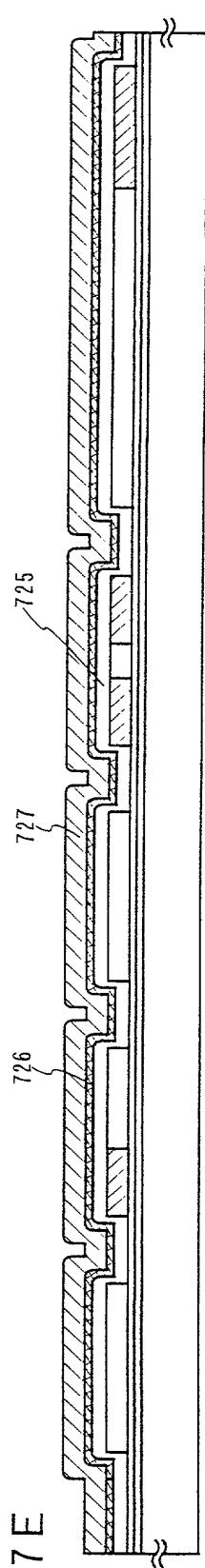


Fig. 8A

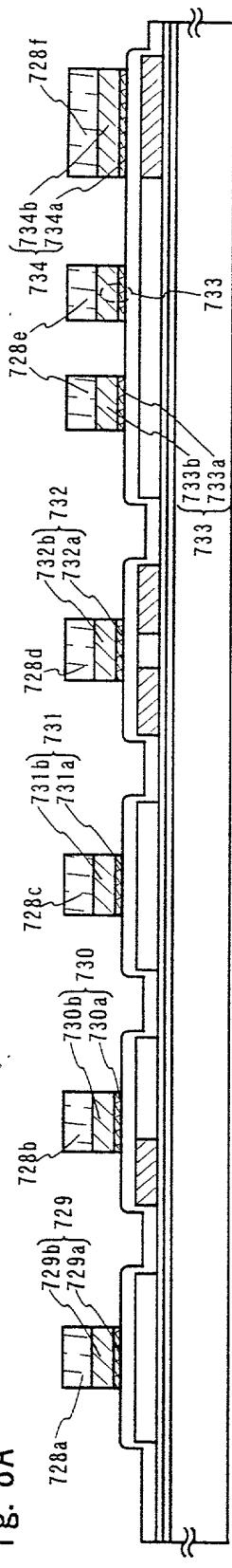


Fig. 8B

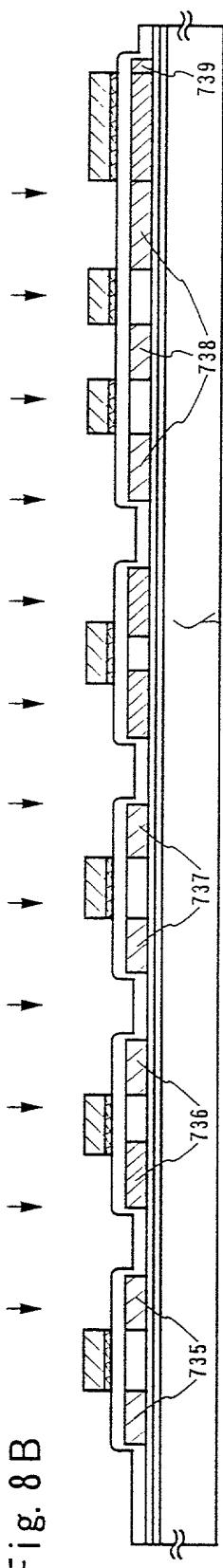


Fig. 8C

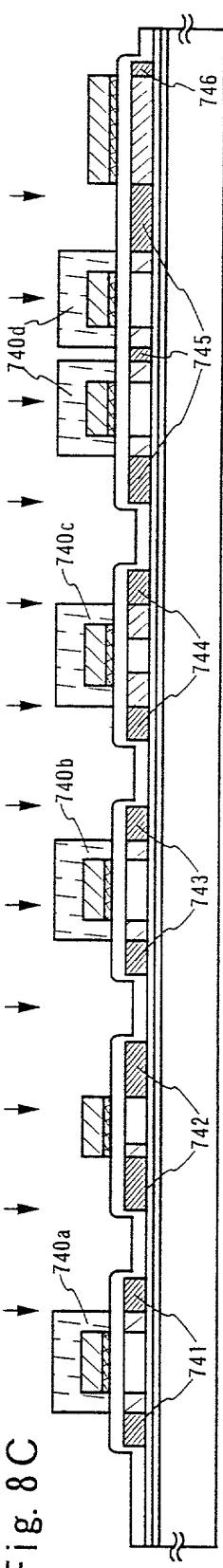


Fig. 8D

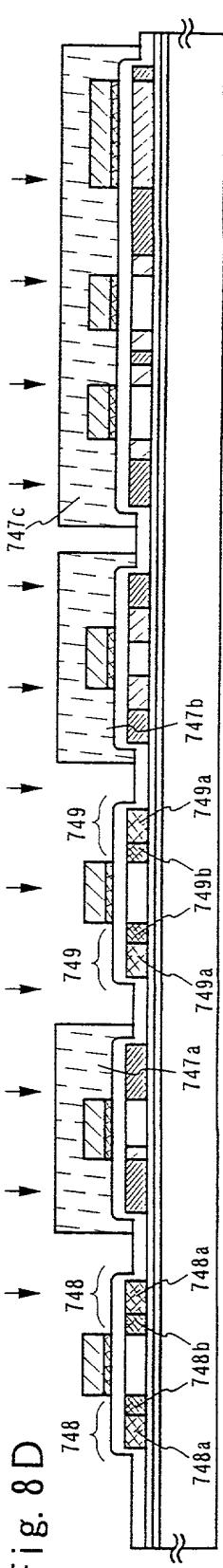


Fig. 9 A

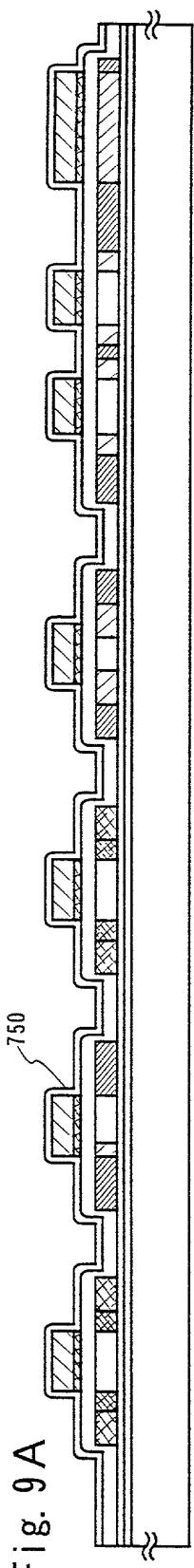


Fig. 9 B

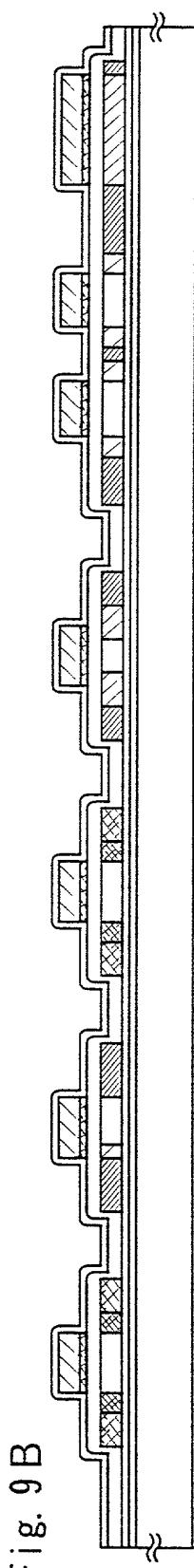
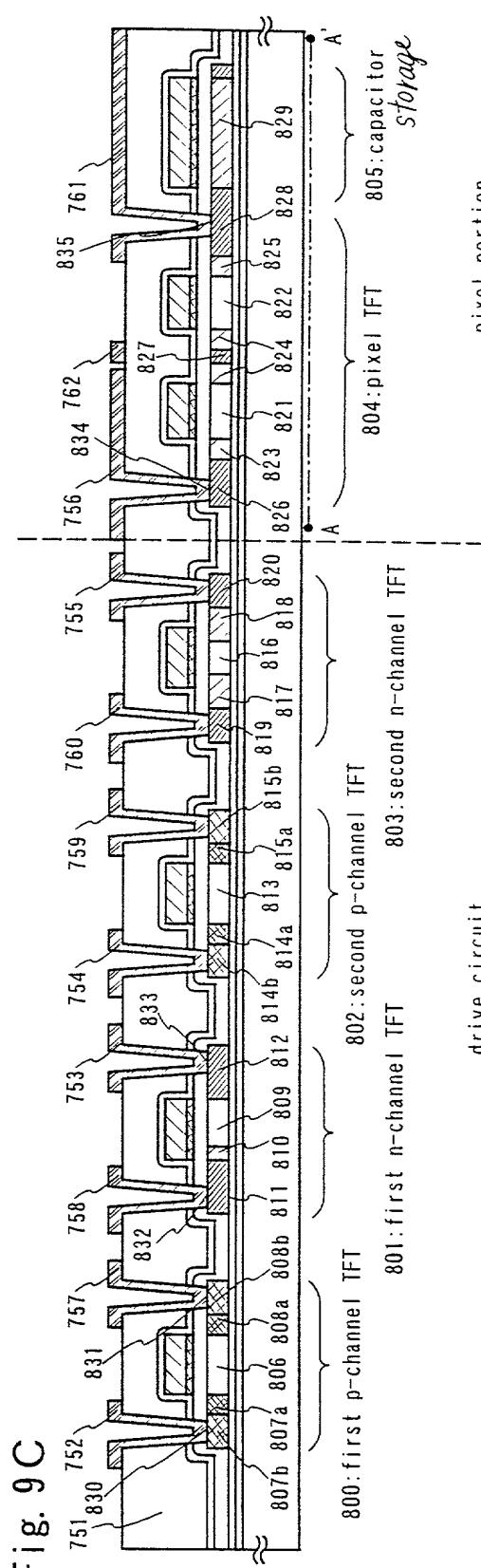


Fig. 9 C



drive circuit

pixel portion

805: capacitor
Storage

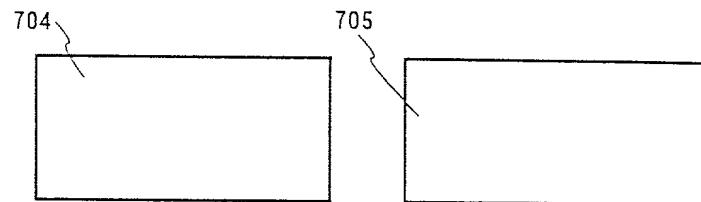


Fig. 10 A

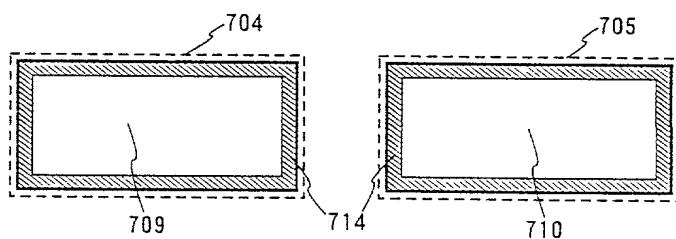


Fig. 10 B

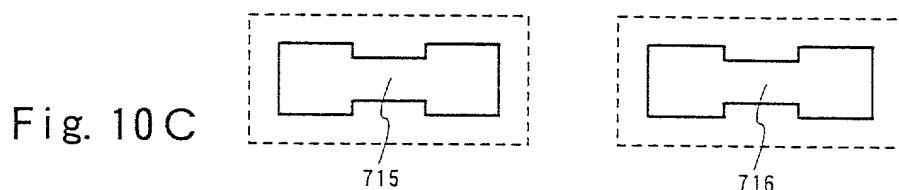


Fig. 10 C

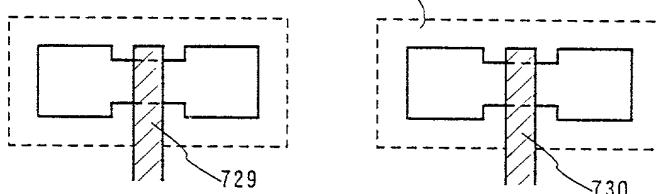


Fig. 10 D

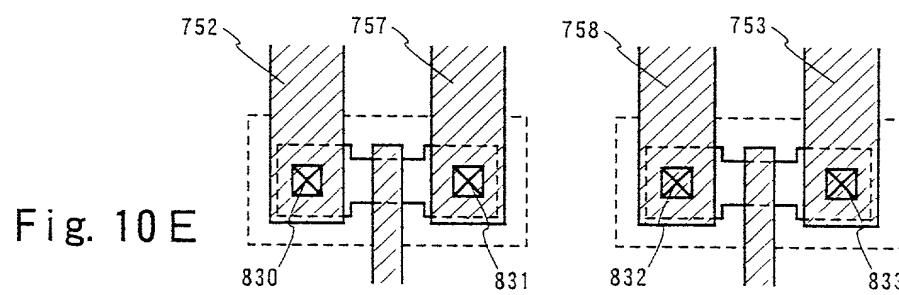
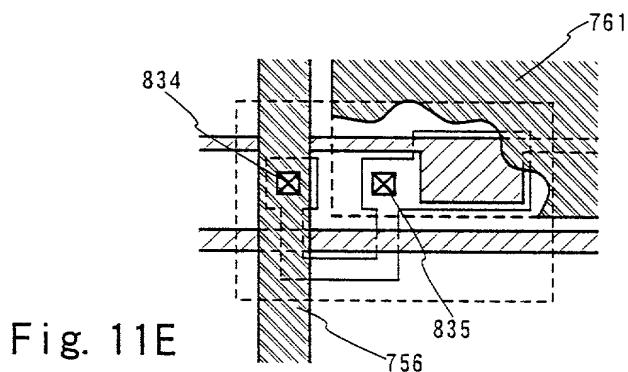
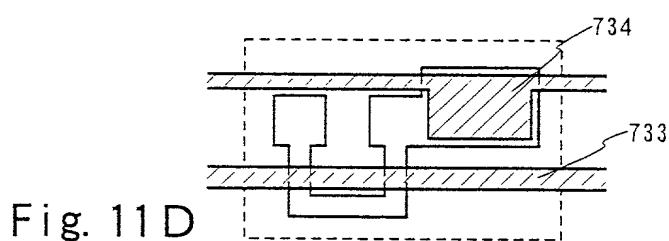
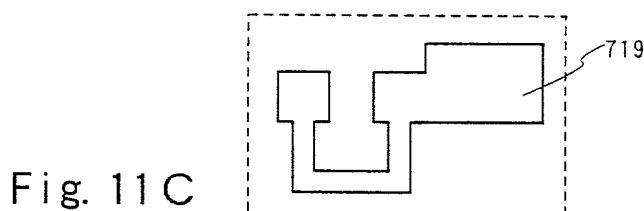
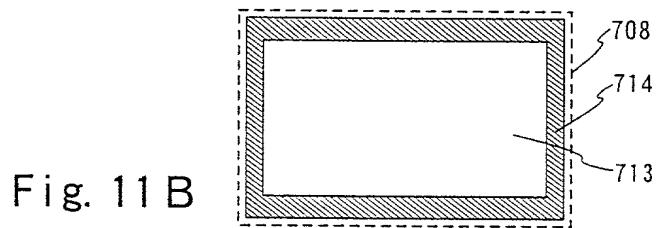
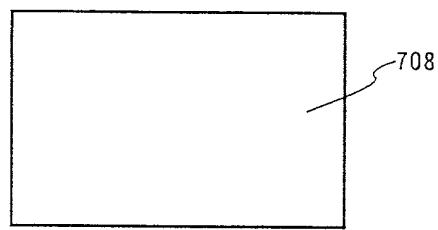


Fig. 10 E



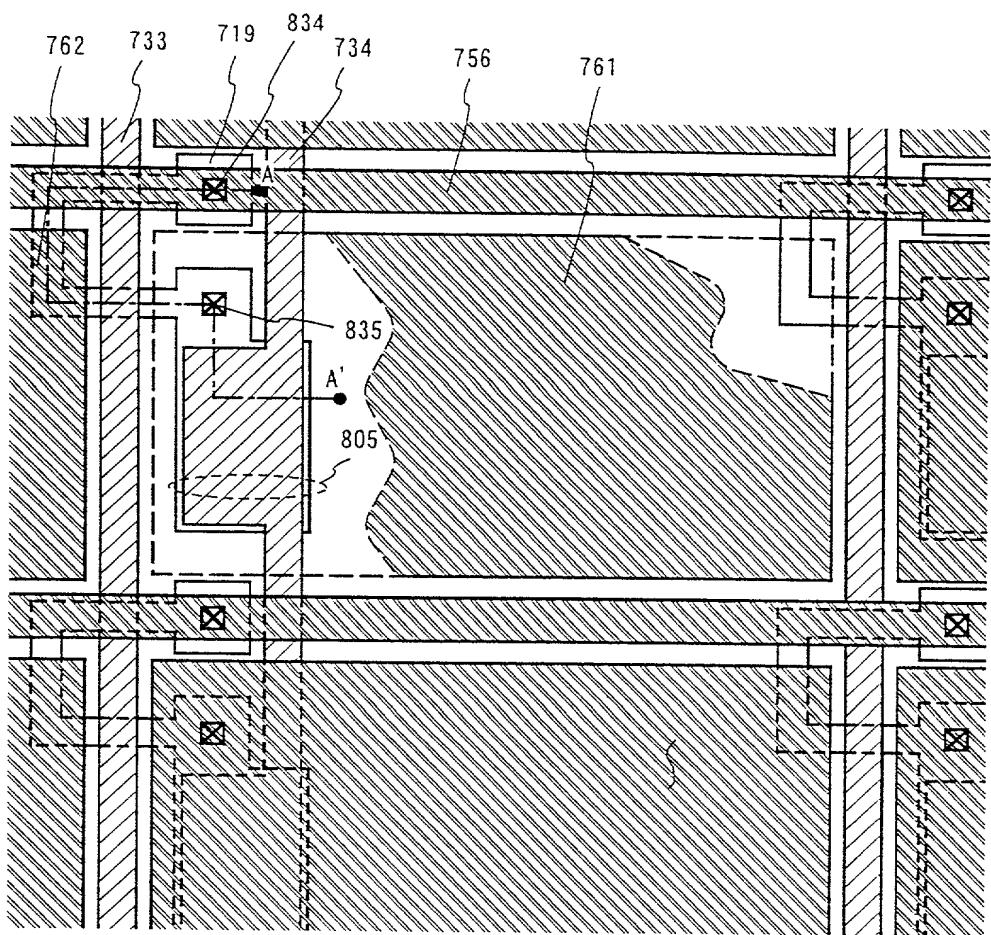
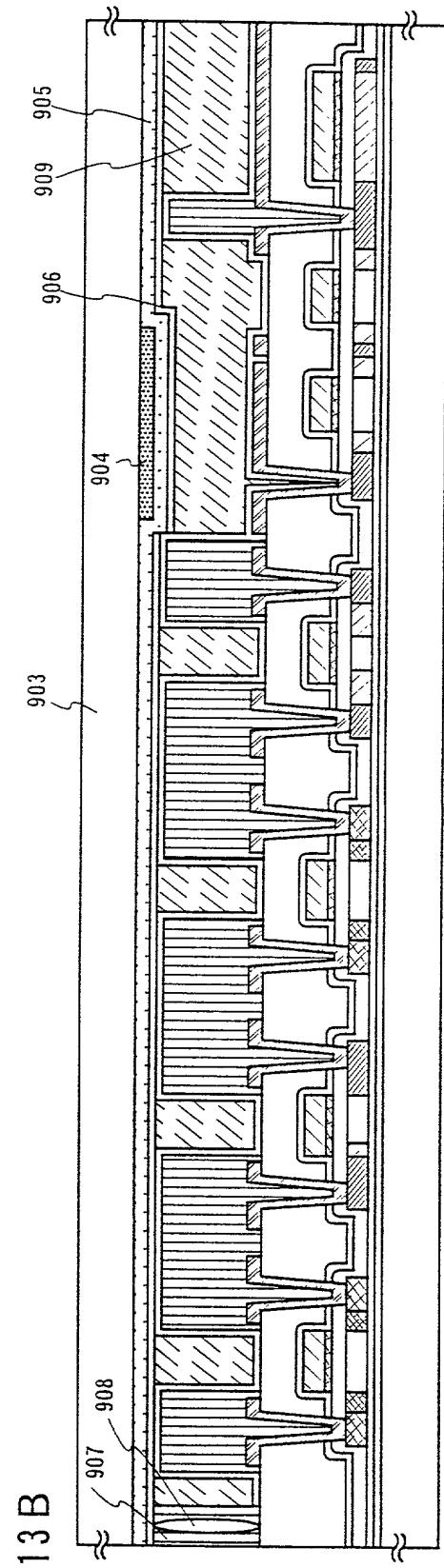
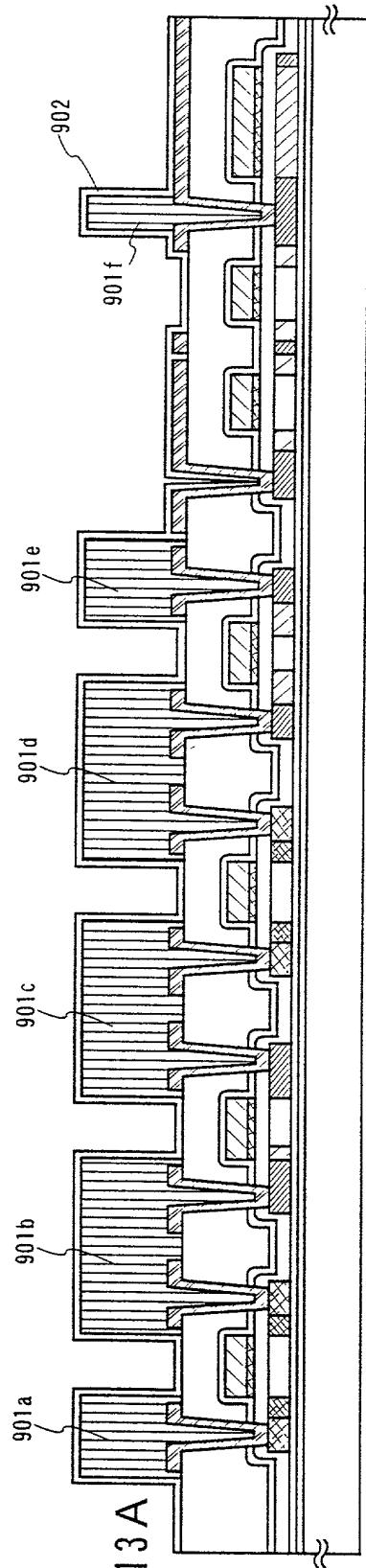


Fig. 12



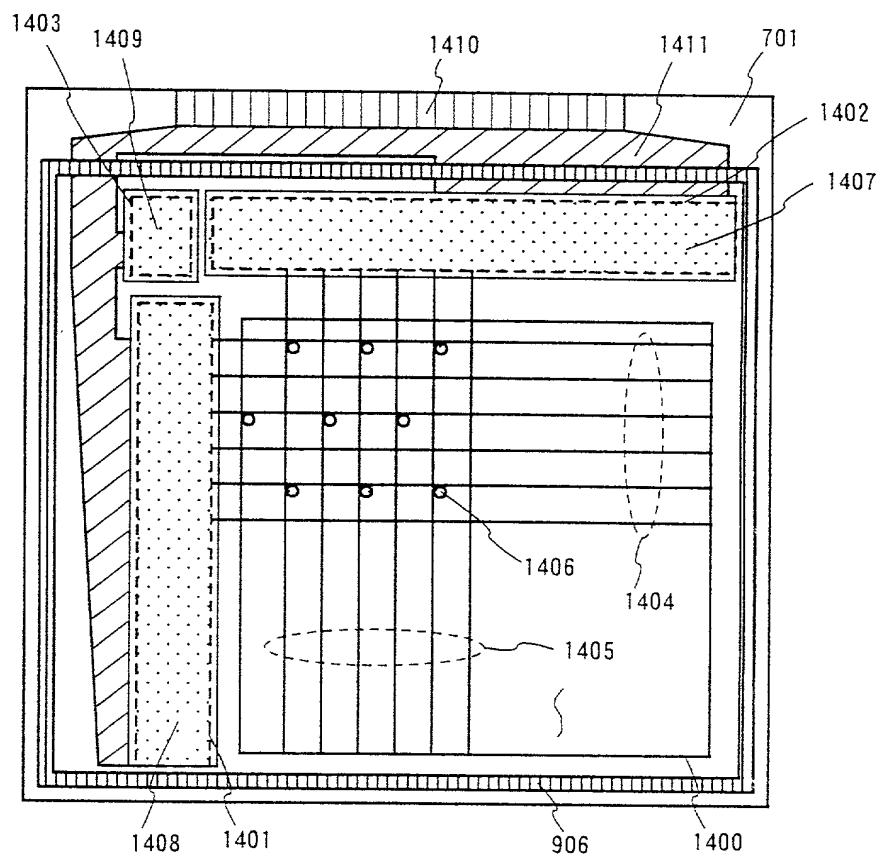


Fig. 14

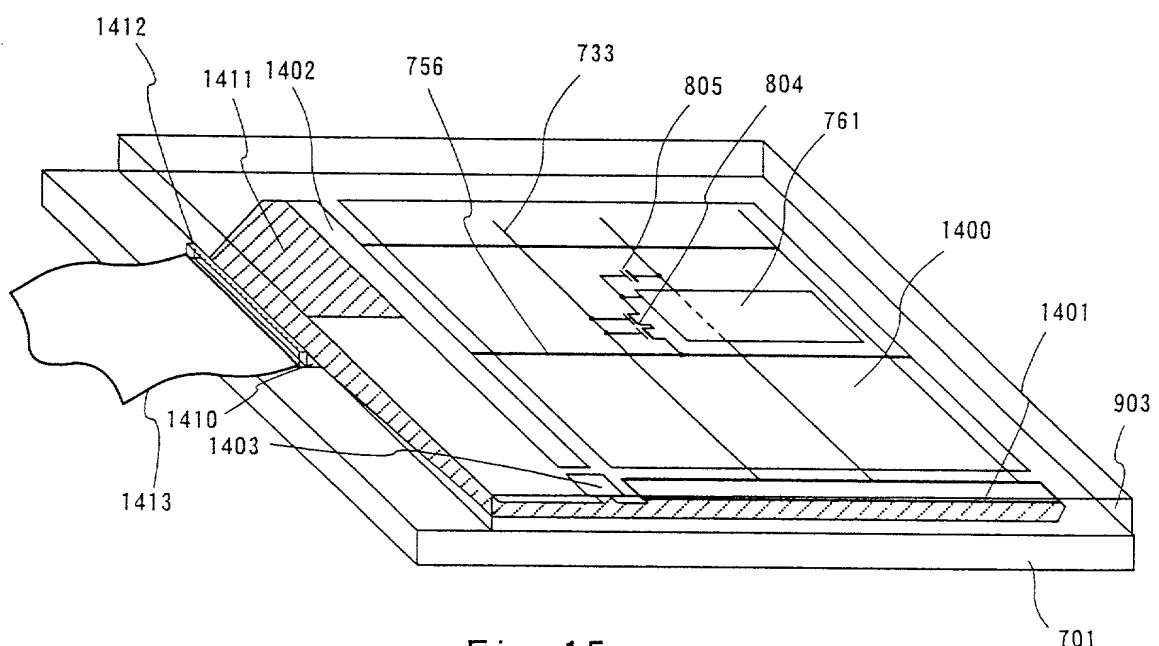
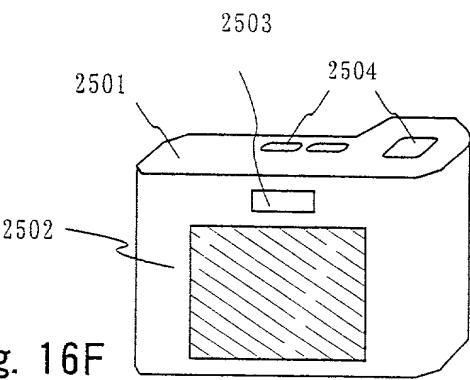
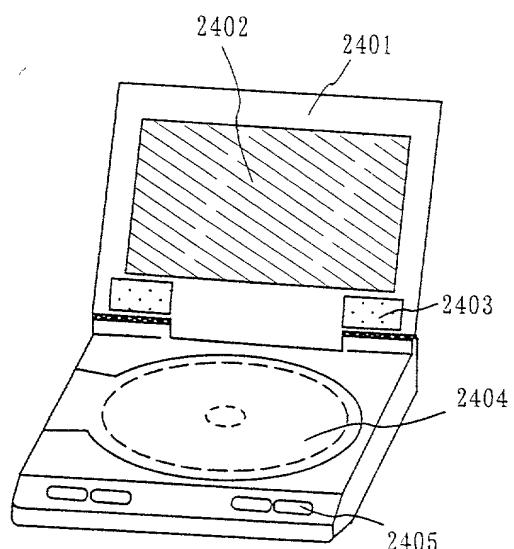
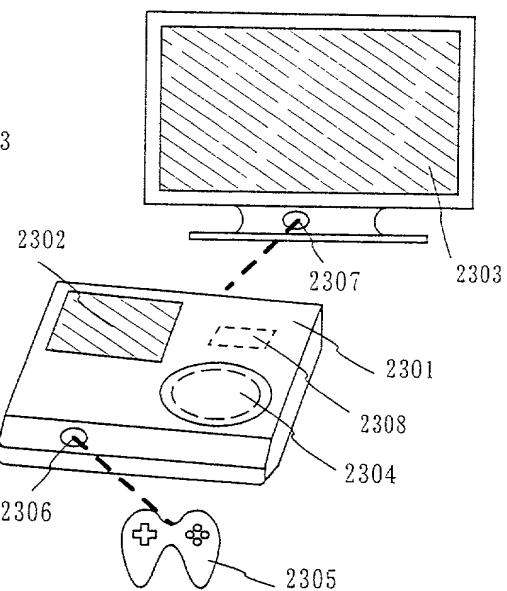
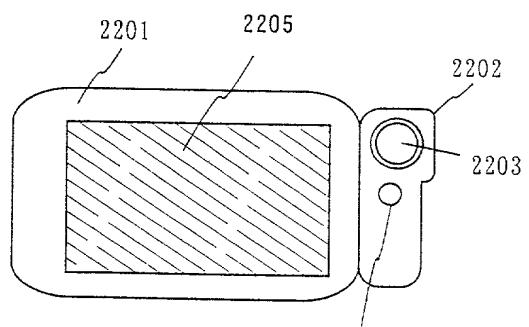
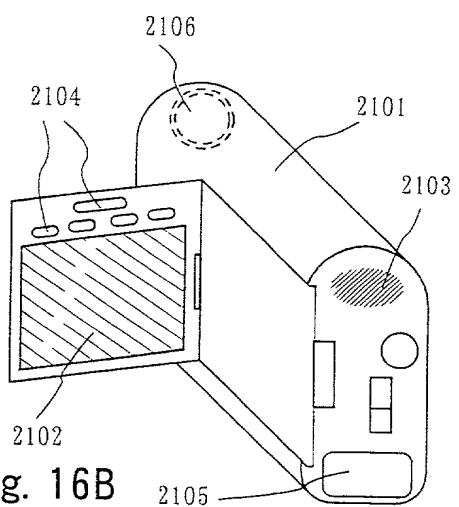
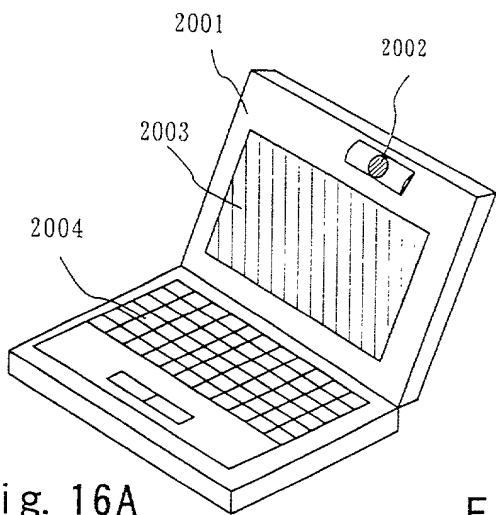


Fig. 15



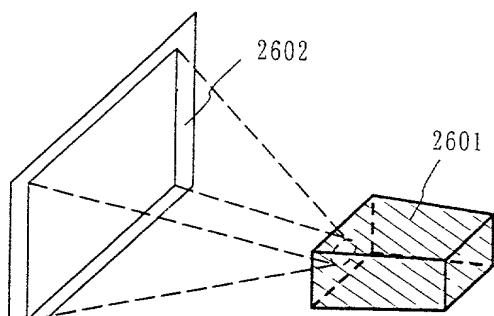


Fig. 17A

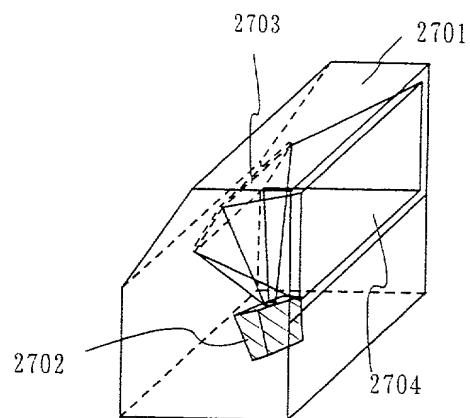


Fig. 17B

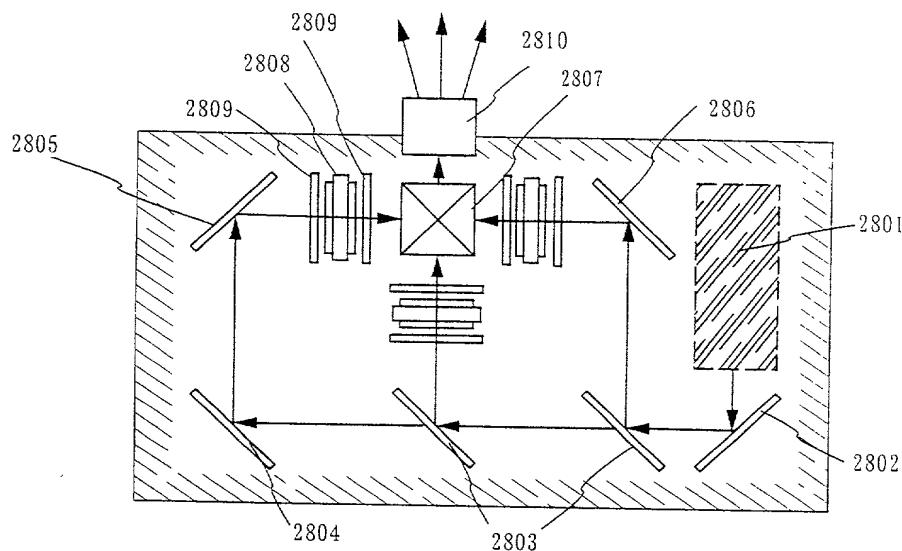


Fig. 17C

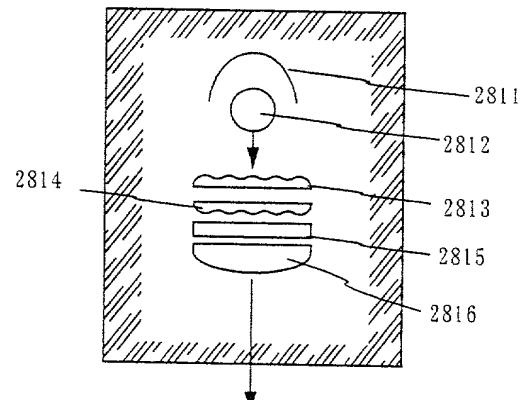


Fig. 17D

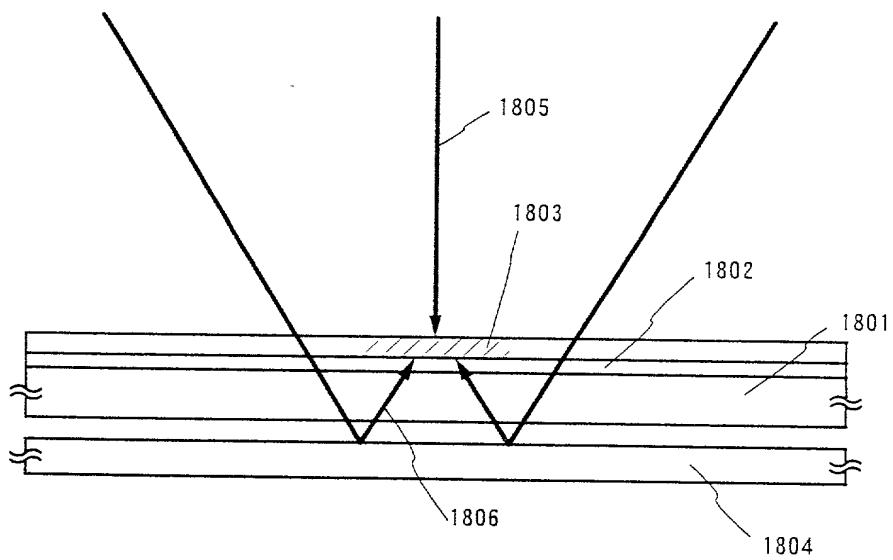


Fig. 18